Inventor:

Gurtej S. Sandhu et al.

Methods of Forming A Thin Film Transistor

Assignee: Micron Technology, Inc.



INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. identified application is a divisional application of co-pending application Serial No. 09/837,645, filed April 17, 2001. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

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	AL			7/90						Yes	No	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.												

Form PTO-144	9		U P/	S. DEBARTMENT OF COM	MERCE OFFICE	ATTY. DOCKET NO MI22-1780		PRIORITY SERIAL NO. 09/837,645			
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